



2823  
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**Amendment/Response Transmittal**

In re application of: Salvatore N. Storino, et al.

Group Art Unit: 2823

Serial No.: 09/751,163

Examiner: K. Nguyen

Filed: December 29, 2000

For: METHOD FOR ELIMINATION OF  
PARASITIC BIPOLAR ACTION IN SILICON  
ON INSULATOR (SOI) DYNAMIC LOGIC  
CIRCUITS

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

Transmitted herewith for filing is an Amendment/Response in the above-identified Application.

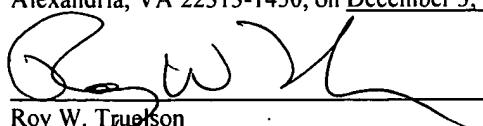
- No additional fee is required.  
 The filing fee has been calculated as follows:

	Claims Remaining After Amendment	Highest Number Previously Paid For	Present Extra
Total	21	- 21	0
Independent	3	- 3	0
<input type="checkbox"/> First Presentation of Multiple Dependent Claim			

Other Than Small Entity	
Rate	Additional Fee
x \$18.00=	0.00
x \$84.00=	0.00
\$280.00	0.00
TOTAL	0.00

**CERTIFICATE OF MAILING UNDER  
37 C.F.R. 1.8(a)**

I hereby certify that the enclosed or attached correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on December 3, 2003.



Roy W. Truelson

Request for Extension of Time:

Applicants respectfully request a \_\_\_ month extension of time to respond to the Office Action dated \_\_\_/\_\_\_/. Please charge Deposit Account 09-0465 in the amount of \$ \_\_\_\_\_. A duplicate copy of this sheet is enclosed.

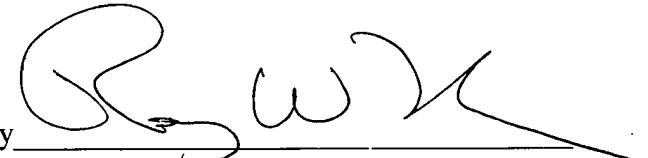
Deposit Account Authorization:

- Please charge Deposit Account No. 09-0465 in the amount of \$ \_\_\_\_\_, the Additional Fee calculated above. A duplicate copy of this sheet is enclosed.
- The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account 09-0465. A duplicate copy of this sheet is enclosed.
- Any additional filing fees required under 37 C.F.R. §1.16.
- Any patent application processing fees under 37 C.F.R. §1.17.

Respectfully submitted,

SALVATORE N. STORINO, et al.

Date: December 3, 2003

By   
Roy W. Truelson  
Registration No.: 34,265

(507) 289-6256 voice/fax

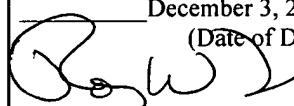
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Salvatore N. Storino, et al. : Date: December 3, 2003  
 Group Art Unit: 2823 : IBM Corporation  
 Examiner: K. Nguyen : Intellectual Property Law  
 Serial No.: 09/751,163 : Dept. 917, Bldg. 006-1  
 Filed: December 29, 2000 : 3605 Highway 52 North  
 Title: METHOD FOR ELIMINATION OF  
 PARASITIC BIPOLAR ACTION IN  
 SILICON ON INSULATOR (SOI)  
 DYNAMIC LOGIC CIRCUITS : Rochester, MN 55901

Commissioner for Patents  
 P.O. Box 1450  
 Alexandria, VA 22313-1450

Sir:

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December 3, 2003  
 (Date of Deposit)  
  
 Roy W. Truelson      12/3/03  
 Date

**AMENDMENT**

This paper is being filed in response to the office action dated November 3, 2003. Please amend the above identified application as follows.

Docket No.: RO998-200B  
 Serial No.: 09/751,163

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